



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#20/F
3/6/3
Jenkins

In re Application of:

Ramsbey et al.

Serial No.: 09/430,366

Filed: October 28, 1999

For: **Method Of Making A Memory Cell
With Polished Insulator Layer**

Art Unit: 2813

Examiner: Chen, Jack S. J.

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FEB 28 2003
TECHNOLOGY CENTER 2800

AMENDMENT AND RESPONSE TO OFFICE ACTION

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Dear Sir:

This is in response to the Office Action dated December 03, 2002 in the above-referenced patent application. Please enter and consider the following amendments and remarks.

Please change the Attorney Docket No. in this application from "M-7523 US" to

--0180164--.